



UNIVERSITY OF CALCUTTA  
Department of Radio Physics and Electronics,

Ref No: SMDP/Advt/32/18

Date: 16/07/2018

Interested candidates are requested to appear in **Walk-in-Interview** for the following 1 (One) temporary post in the Special Manpower Development Programme for Chip to System Design (SMDP-C2SD) at University of Calcutta (Dept. of Radio Physics and Electronics).

**Laboratory Engineer: No of post – One**

**Essential Qualification:** First class M.E./M.Tech. in Radio Physics and Electronics/Electronics and Communication Engineering/Computer Science and Engineering/Information Technology/Equivalent from a recognized University.

**Essential Experience:** 2 years' experience in teaching/research and/or industrial experience in the field of VLSI CAD tools, FPGA/front-end design and verification.

**Desirable Experience:** Good knowledge of FPGA/ASIC design flow, installation and demonstration of EDA tools like Xilinx, Mentor Graphics, Cadence, Synopsys etc.

**Salary: Rs. 34,000/- (consolidated)**

**Duration:** The appointment is purely in temporary basis. The tenure of the appointments will be initially for one year, which may be renewed further on the basis of successful performance of the candidate.

**Date, Time and Venue of the Walk-in- Interview:** 27.07.2018, 27<sup>th</sup> July 2018 (Friday) 3.00pm, A.N.Daw Meeting Room, Radio Physics and Electronics Department, University of Calcutta.

Competent Candidates are requested to bring two hard copies of bio-data along with attested copies of mark sheets, testimonials for essential and desirable qualifications etc., to submit along with original mark sheets and certificates for verification. **Soft copy of the bio-data including contact numbers may be sent through e-mail with subject "Laboratory Engineer under the SMDP-C2SD Project", 3 days prior to the date of interview to: Shri. Samik Basu, Email id: smdpc2sd.caluniv@gmail.com**

No TA/DA will be admissible for the interview

Dr. Soumya Pandit  
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